

**AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:**

1. (Currently Amended) A liquid crystal display for preventing static electricity, comprising:
  - a picture display part having a plurality of liquid crystal cells;
  - a signal pad part for applying a driving signal to be inputted to signal lines of the picture display part,
    - wherein the signal pad part includes:
      - a plurality of pads connected to respective ones of the signal lines; and
      - a static electricity prevention circuit having a thin film transistor with a floating gate for connecting at least one of said pads with an equipotential line in the presence of static electricity; and
      - a link line connecting the one of said pads to the thin film transistor respectively,  
wherein the link line is connected to a node between an outer edge of the picture display part and thin film transistor, and to an outer edge of the one of said pads near to the equipotential line.  
~~wherein the equipotential line is disposed at an outer portion between an outer edge of the liquid crystal and the signal pad part.~~
  2. (Original) The liquid crystal display according to claim 1, wherein the static electricity prevention circuit causes the one of said pads to be insulated from the equipotential line and the other pads when the driving signal is applied to the one of said pads so as to apply the driving signal to the signal lines of the picture display part.
  3. (Original) The liquid crystal display according to claim 1, the static electricity prevention circuit, further comprising:

a first capacitor connected between the floating gate and a first terminal of the thin film transistor connected to the equipotential line; and

a second capacitor connected between the floating gate and a second terminal of the thin film transistor connected to the one of said pads.

4. (Previously Presented) The liquid crystal display according to claim 1, further comprising:

a second static electricity prevention circuit formed between the one of said pads and a first drive voltage supply line and a second drive voltage supply line for bypassing the static electricity flowing from the one of said pads into at least one of the first and second drive voltage supply lines.

5. (Currently Amended) The liquid crystal display according to claim 4 1, wherein the one of said pads is connected to the static electricity prevention circuit through a link line, wherein the link line has a first portion between a grinding line and the equipotential line.

6. (Original) The liquid crystal display according to claim 5, wherein the link line further comprises a second portion spaced apart from the first portion and having a first part outside the grinding line and a second part between the grinding line and the static electricity prevention circuit.

7. (Canceled)

8. (Canceled)

9. (Currently Amended) The A liquid crystal display according to claim 8, for preventing static electricity, comprising:

a picture display part having a plurality of liquid crystal cells;

a signal pad part for applying a driving signal to be inputted to signal lines of the picture display part,

wherein the signal pad part includes:

a plurality of pads connected to respective ones of the signal lines;  
a static electricity prevention circuit having a thin film transistor with a floating gate for connecting at least one of said pads with an equipotential line in the presence of static electricity;  
a link line connecting the one of said pads to the thin film transistor respectively, wherein the link line is connected to a node between an outer edge of the picture display part and thin film transistor, and to an outer edge of the one of said pads near to the equipotential line; and  
a second static electricity prevention circuit formed between the one of said pads and a first drive voltage supply line and a second drive voltage supply line for bypassing the static electricity flowing from the one of said pads into at least one of the first and second drive voltage supply lines;

wherein the ~~first link line and the second link line each have~~ has a first portion between a grinding line and the equipotential line.

10. (Currently Amended) The liquid crystal display according to claim 9, wherein the first link line further comprises ~~a second portion spaced apart from the first portion and having a first part outside the grinding line and a second part between the grinding line and the static electricity prevention circuit; and wherein the second link line further comprises a second portion spaced apart from the first portion and having a first part outside the grinding line and a second part between the grinding line and the second static electricity prevention circuit.~~

11. (Previously Presented) The liquid crystal display according to claim 5, wherein the one of said pads is electrically separated from the static electricity prevention circuit, the second static electricity prevention circuit, and the signal lines of the picture display part by the grinding process in which the equipotential line is removed.

12. (Previously Presented) The liquid crystal display according to claim 10, wherein the one of said pads is electrically separated from the static electricity prevention circuit, the second static electricity prevention circuit, and the signal lines of the picture display part by the grinding process in which the equipotential line is removed.

13. (Original) The liquid crystal display according to claim 1, further comprising:  
at least one resistor connected between the static electricity prevention circuit and the  
picture display part for limiting current.

14. (Currently Amended) The liquid crystal display according to claim 8 or 9, further  
comprising:

at least one resistor connected between the one of said pads and the picture display part  
for limiting current.

15. (Original) The liquid crystal display according to claim 1, further comprising:  
at least one resistor connected between the one of said pads and the equipotential line.

16. (Original) The liquid crystal display according to claim 1, wherein the signal pad  
part includes:

a link pad part having a plurality of link pads that connect the signal lines of the picture  
display part with a drive circuit.

17. (Original) The liquid crystal display according to claim 16, wherein the signal pad  
part includes:

a test pad part having a plurality of test pads connected to the signal lines of the picture  
display part for inspecting the liquid crystal display panel.

18. (Currently Amended) A method for preventing static electricity during  
manufacture of a liquid crystal display device, comprising:

providing a picture display part having a plurality of liquid crystal cells;

providing a signal pad part for applying a driving signal to be inputted to signal lines of  
the picture display part,

wherein the signal pad part includes a plurality of pads connected to respective ones of  
the signal lines, and a static electricity prevention circuit having a thin film transistor with a  
floating gate for connecting at least one of said pads with an equipotential line in the presence of  
static electricity, and a link line connecting the pads to the thin film transistor respectively,

wherein the link line is connected to a node between an outer edge of the picture display part and thin film transistor, and to an outer edge of the one of said pads near to the equipotential line

~~wherein the equipotential line is disposed at an outer portion between an outer edge of the liquid crystal display and the signal pad part.~~

19. (Original) The method according to claim 18, wherein the static electricity prevention circuit causes the one of said pads to be insulated from the equipotential line and the other pads when the driving signal is applied to the one of said pads so as to apply the driving signal to the signal lines of the picture display part.

20. (Original) The method according to claim 18, wherein the static electricity prevention circuit includes:

a first capacitor connected between the floating gate and a first terminal of the thin film transistor connected to the equipotential line; and

a second capacitor connected between the floating gate and a second terminal of the thin film transistor connected to the one of said pads.

21. (Previously Presented) The method according to claim 18, wherein the signal pad part further includes a second static electricity prevention circuit formed between the one of said pads and a first drive voltage supply line and a second drive voltage supply line for bypassing the static electricity flowing from the one of said pads into at least one of the first and second drive voltage supply lines.

22. (Currently Amended) The method according to claim 21, ~~wherein the one of said pads is connected to the static electricity prevention circuit through a link line~~, wherein the link line has a first portion between a grinding line and the equipotential line.

23. (Original) The method according to claim 22, wherein the link line further comprises a second portion spaced apart from the first portion and having a first part outside the

grinding line and a second part between the grinding line and the static electricity prevention circuit.

24. (Original) The method according to claim 18, wherein the one of said pads is connected to a node between the static electricity prevention circuit and the picture display part.

25. (Canceled)

26. (Currently Amended) ~~The A method according to claim 25, for preventing static electricity during manufacture of a liquid crystal display device, comprising:~~

~~providing a picture display part having a plurality of liquid crystal cells;~~

~~providing a signal pad part for applying a driving signal to be inputted to signal lines of the picture display part,~~

~~wherein the signal pad part includes a plurality of pads connected to respective ones of the signal lines, a static electricity prevention circuit having a thin film transistor with a floating gate for connecting at least one of said pads with an equipotential line in the presence of static electricity, and a link line connecting the one of said pads to the thin film transistor respectively,~~

~~wherein the link line is connected to a node between an outer edge of the picture display and thin film transistor, and to an outer edge of the one of said pads near to the equipotential line,~~

~~wherein the signal pad part further includes a second static electricity prevention circuit formed between the one of said pads and a first drive voltage supply line and a second drive voltage supply line for bypassing the static electricity flowing from the one of said pads into at least one of the first and second drive voltage supply lines; and~~

~~wherein the first link line and the second link line each have has a first portion between a grinding line and the equipotential line.~~

27. (Currently Amended) The method according to claim 26, wherein the ~~first~~ link line further comprises ~~a second portion spaced apart from the first portion and having a first part outside the grinding line and a second part between the grinding line and the static electricity prevention circuit; and wherein the second link line further comprises a second portion spaced~~

~~apart from the first portion and having a first part outside the grinding line and a second part between the grinding line and the second static electricity prevention circuit.~~

28. (Previously Presented) The method according to claim 22, wherein the one of said pads is electrically separated from the static electricity prevention circuit, the second static electricity prevention circuit, and the signal lines of the picture display part by the grinding process in which the equipotential line is removed.

29. (Previously Presented) The method according to claim 23, wherein the one of said pads is electrically separated from the static electricity prevention circuit, the second static electricity prevention circuit, and the signal lines of the picture display part by the grinding process in which the equipotential line is removed.

30. (Original) The method according to claim 18, further comprising:  
providing at least one resistor connected between the static electricity prevention circuit and the picture display part for limiting current.

31. (Previously Presented) The method according to claim 18, further comprising:  
providing at least one resistor connected between the one of said pads and the picture display part for limiting current.

32. (Original) The method according to claim 18, further comprising:  
providing at least one resistor connected between the one of said pads and the equipotential line.

33. (Canceled)

34. (Currently Amended) The liquid crystal display according to claim ~~33~~ 18, wherein the signal pad part includes:

a test pad part having a plurality of test pads connected to the signal lines of the picture display part for inspecting the ~~liquid crystal display panel~~ picture display part.